

**pcb scope / logic analyzer hardware design description** - pcb scope / logic analyzer hardware design description introduction the pcb scope is the result of a challenge i set for myself "to build a practically usable oscilloscope" **an-926 radiation design considerations using cmos logic ...** - t1/f/11650 radiation design considerations using cmos logic an-926 national semiconductor application note 926 michael maher january 1994 radiation design **digital phase locked loop induction motor speed controller ...** - digital phase locked loop induction motor speed controller: design and experiments mouna ben hamed and lassaad sbita 160 introduction three phase imms are the most widely used electrical motors due to their ruggedness, **in our industry, there's nothing wrong with being ...** - ai-tek speed sensors >> tachometers >> accessories in our industry, there's nothing wrong with being predictable. distributed by inc, airpax **accelerating opencv applications with zynq-7000 all ...** - reference design xapp1167 (v3.0) june 24, 2015 xilinx 2 the design flow for this application note generally follows the steps below: 1. develop and execute an opencv application on desktop. **designing for low power and estimating battery life for ...** - designing for low power and estimating battery life for ble applications cypress document no. 001-92584 rev. \*d 3 table 1 summarizes the clock sources, their use in the system, and specific system constraints on them. **displayport devcon presentation, edp, dec 2010 v3** - quick summary of edp ! embedded displayport (edp) was developed to be used specifically in embedded display applications ! notebook, netbook, and notepad pcs **spartan-3an fpga family data sheet (ds557)** - spartan-3an fpga family: introduction and ordering information ds557(v4.3) january 9, 2019 xilinx product specification 3 architectural overview **absolute encoder design: magnetic or optical?** "ic-haus- the demands made of today's absolute position sensors with regard to measurement accuracy, size, and cost are often extremely diverse. benefits are only forthcoming where an appropriate system **digital excitation control system decs-400** - introduction this instruction manual provides information about the operation and installation of the decs-400 digital excitation control system. **and use of vlsi circuit design lesson 7: technology system ...** - 2008 chapter-1107: "embedded systems - ", raj kamal, publs.: mcgraw-hill education 2 vlsi chip integration of high-level components **digital epic-2 intelligent valve position transmitter** - 6 | position transmitter the optimum balance of cost and functionality westlock controls "digital epic-2 is a second generation, transmitter-based intelligent device that provides **interface products design guide using can, lin and ...** - interface products design guide using can, lin and infrared connectivity analog and interface product solutions lin transceiver mcp201 general purpose i/o expander **the theory of digital down conversion - hunteng** - 1 v1.2 0603 the theory of digital down conversion rev 1.2 t.hollis/r.weir 26-06-2003 introduction the advent of larger and faster xilinx fpgas has opened up the field of digital signal processing.

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